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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10019696	06/20/2002	257	679	2811	T. T. VU

**APPLICANTS: Finn David; Rietzler Manfred;

**CONTINUING DATA VERIFIED:

THIS APPLICATION IS A 371 OF PCT/DE00/01396 05/04/2000

** FOREIGN APPLICATIONS VERIFIED:

GERMANY 19920593.0 05/05/1999

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	
Foreign priority claimed	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO	
35 USC 119 conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no		70357
Verified and Acknowledged Examiner's initials	av		
TITLE : Chip carrier a chip module and method of manufacturing the chip module			
U.S. DEPT. OF COMM /PAT. & TM-PTO-436L (Rev. 12-94)			

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
		DRAWING	
Assistant Examiner	Sheets Drawn	Fig.s Drawn	Print Fig.
ISSUE FEE			
Amount Due	Date Paid		
		Primary Examiner	
TERMINAL		Application Examiner	
DISCLAIMER		PREPARED FOR ISSUE	
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